

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6586	wireless AND (ad ADJ hoc)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 21:47
L2	6395	wireless AND (ad ADJ hoc) AND network	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 21:47
L3	1992	wireless AND (ad ADJ hoc) AND network AND transceiver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 21:47
L4	1225	wireless AND (ad ADJ hoc) AND network AND transceiver AND controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 21:47
L5	6586	wireless AND (ad ADJ hoc)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 21:47
L6	6395	wireless AND (ad ADJ hoc) AND network	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 21:47
L7	1992	wireless AND (ad ADJ hoc) AND network AND transceiver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 21:47
L8	1225	wireless AND (ad ADJ hoc) AND network AND transceiver AND controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 21:47
L9	95	wireless AND (ad ADJ hoc) AND network AND transceiver AND (controller WITH (prevent\$4 OR stop\$4 OR cancel\$5 OR delay\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 21:47

L10	62	wireless AND (ad ADJ hoc) AND network AND transceiver AND (controller WITH battery)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 21:47
L11	179	wireless AND network AND transceiver AND (controller WITH (prevent\$4 OR stop\$4 OR cancel\$5 OR delay\$4) WITH (battery OR power OR life))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 21:47
L12	11	wireless AND (ad ADJ hoc) AND network AND transceiver AND (controller WITH (prevent\$4 OR stop\$4 OR cancel\$5 OR delay\$4) WITH (battery OR power OR life))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 21:47
L13	53	wireless AND (ad ADJ hoc) AND network AND transceiver AND (controller WITH battery) AND memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 21:47
L14	19	wireless AND network AND transceiver AND (controller ADJ3 (prevent\$4 OR stop\$4 OR cancel\$5 OR delay\$4) ADJ5 (battery OR power OR life))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 21:47